

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.-12. (Canceled)

13. (Currently Amended) A semiconductor device having an active matrix display device, said display device comprising:

~~at least one~~ a first thin film transistor formed over ~~a substrate~~ an insulating surface;

a pixel electrode electrically connected to said first thin film transistor;

a driver circuit including ~~at least one~~ a second thin film transistor formed over said ~~substrate~~ insulating surface for driving said ~~at least one~~ first thin film transistor,

wherein ~~at least one of~~ said first thin film transistor ~~comprising~~ comprises:

a first semiconductor island over ~~[[an]]~~ the insulating surface;

source and drain regions formed in the semiconductor island;

a first channel forming region in the first semiconductor island between the source and drain regions;

a pair of lightly doped regions formed between the first channel forming region and the source and drain ~~regions~~ regions, wherein an impurity concentration in the lightly doped regions is smaller than that in the source and drain regions;

a gate electrode formed over the semiconductor island with a gate insulating film interposed therebetween,

wherein said gate electrode comprises at least a first conductive layer and a second conductive layer formed on the first conductive layer, said first conductive layer having a pair of tapered ~~portions~~, portions which extend beyond side edges of the second conductive layer,

wherein the pair of lightly doped regions has a pair of first portions which are overlapped by the pair of tapered portions of the first conductive layer, and a pair of second portions which extend beyond side edges of the first conductive layer, and

wherein ~~[[the]]~~ an impurity concentration in the pair of first portions is smaller than ~~[[the]]~~ an impurity concentration in the pair of second portions.

14. (Currently Amended) A device according to claim 13, wherein an angle between top surfaces of the tapered portions of the first conductive layer and a top surface of the gate insulating film is in a range of 3 to 60 degrees.

15. (Currently Amended) A device according to claim 13, wherein the first semiconductor island ~~[[is a]]~~ comprises crystalline silicon ~~island~~.

16. (Currently Amended) A device according to claim 13, wherein the first conductive layer includes at least one selected from the group consisting of chromium (Cr), tantalum ~~[[Ta]]~~ (Ta), an n-type silicon containing phosphorus, titanium (Ti), tungsten (W), and molybdenum (Mo) while the second conductive layer includes at least one selected from the group consisting of aluminum (Al), copper (Cu), chromium (Cr), tantalum (Ta), titanium (Ti), tungsten (W), molybdenum (Mo), an n-type silicon containing phosphorus, and silicide.

17. (Previously Presented) A device according to claim 13, wherein the semiconductor device is one selected from the group consisting of a video camera, a digital camera, a rear-type projector, a front-type projector, a head mount display, a goggle-type display, a navigation system for vehicles, a personal computer, a mobile computer, a cellular phone, and an electronic book.

18.-45. (Canceled)

46. (Currently Amended) A semiconductor device having an active matrix display device, said display device comprising:

~~at least one~~ a first thin film transistor formed over ~~a substrate~~ an insulating surface;

a pixel electrode electrically connected to said first thin film transistor;

a driver circuit including ~~at least one~~ a second ~~n-channel~~ thin film transistor and ~~at least one~~ a third ~~p-channel~~ thin film transistor formed over said ~~substrate~~ insulating surface for driving said first thin film transistor, wherein the second thin film transistor is an n-channel thin film transistor and the third thin film transistor is a p-channel thin film transistor.

wherein ~~at least one of~~ said second n-channel thin film transistor ~~comprising~~ comprises:

a first semiconductor island over ~~[[an]]~~ the insulating surface;

first source and drain regions formed in the first semiconductor island;

a first channel forming region in the first semiconductor island between the first source and drain regions;

a pair of lightly doped regions formed between the first channel forming region and the first source and drain regions, wherein an impurity concentration in the lightly doped regions is smaller than that in the first source and drain regions;

a first gate electrode formed over the semiconductor island with a gate insulating film interposed ~~therebetween~~ therebetween.

wherein said first gate electrode comprises at least a first conductive layer and a second conductive layer formed on the first conductive layer, said first conductive layer having a pair of tapered ~~portions~~, portions which extend beyond side edges of the second conductive layer,

wherein the pair of lightly doped regions has a pair of first portions which are overlapped by the pair of tapered portions of the first conductive layer, and a pair of second portions which extend beyond side edges of the first conductive layer,

wherein ~~at least one of~~ said third ~~p-channel~~ thin film transistor ~~comprising~~
comprises:

a second semiconductor island over ~~[[an]]~~ the insulating surface;

second source and drain regions formed in the second semiconductor
island;

a second channel forming region in the second semiconductor island
between the second source and drain regions;

a second gate electrode formed over the second semiconductor island
with ~~[[a]]~~ the gate insulating film interposed ~~therebetween~~ therebetween.

wherein said second gate electrode comprises at least a third conductive layer
and a fourth conductive layer formed on the third conductive layer,

wherein side edges of said third conductive layer are coextensive with side
edges of said fourth conductive layer.

47. (Currently Amended) A device according to claim 46, wherein an angle
between top surfaces of the tapered portions of the first and third conductive ~~[[layer]]~~
layers and a top surface of the gate insulating film is in a range of 3 to 60 degrees.

48. (Currently Amended) A device according to claim 46, wherein the
semiconductor island ~~[[is a]]~~ comprises crystalline silicon island.

49. (Previously Presented) A device according to claim 46, wherein the first
conductive layer includes at least one selected from the group consisting of chromium
(Cr), tantalum (Ta), an n-type silicon containing phosphorus, titanium (Ti), tungsten (W),
and molybdenum (Mo) while the second conductive layer includes at least one selected
from the group consisting of aluminum (Al), copper (Cu), chromium (Cr), tantalum (Ta),
titanium (Ti), tungsten (W), molybdenum (Mo), an n-type silicon containing phosphorus,
and silicide.

50. (Previously Presented) A device according to claim 46, wherein the semiconductor device is one selected from the group consisting of a video camera, a digital camera, a rear-type projector, a front-type projector, a head mount display, a goggle-type display, a navigation system for vehicles, a personal computer, a mobile computer, a cellular phone, and an electronic book.

51. (Currently Amended) A semiconductor device having an active matrix display device, said display device comprising:

~~at least one~~ a first thin film transistor formed over ~~a substrate~~ an insulating surface;

a pixel electrode electrically connected to said first thin film transistor;

a driver circuit including ~~at least one~~ a second thin film transistor formed over said ~~substrate~~ insulating surface for driving said ~~at least one~~ first thin film transistor,

wherein ~~at least one of~~ said second thin film transistor ~~comprising~~ comprises:

a semiconductor island over ~~[[an]]~~ the insulating surface;

source and drain regions formed in the semiconductor island;

a channel forming region in the semiconductor island between the source and drain regions;

a pair of lightly doped regions formed between the channel forming region and the source and drain regions wherein an impurity concentration in the lightly doped regions is smaller than that in the source and drain regions;

a gate electrode formed over the semiconductor island with a gate insulating film interposed therebetween,

wherein said gate electrode comprises at least a first conductive layer and a second conductive layer formed on the first conductive layer, said first conductive layer having a pair of tapered ~~portions~~, portions which extend beyond side edges of the second conductive layer,

wherein the pair of lightly doped regions has a pair of first portions which are overlapped by the pair of tapered portions of the first conductive layer, and a pair of second portions which extend beyond side edges of the first conductive layer, and

wherein ~~[[the]]~~ an impurity concentration in the pair of first portions is smaller than ~~[[the]]~~ an impurity concentration in the pair of second portions.

52. (Currently Amended) A device according to claim 51, wherein an angle between top surfaces of the tapered portions of the first conductive layer and a top surface of the gate insulating film is in a range of 3 to 60 degrees.

53. (Currently Amended) A device according to claim 51, wherein the semiconductor island ~~[[is a]]~~ comprises crystalline silicon ~~island~~.

54. (Previously Presented) A device according to claim 51, wherein the first conductive layer includes at least one selected from the group consisting of chromium (Cr), tantalum (Ta), an n-type silicon containing phosphorus, titanium (Ti), tungsten (W), and molybdenum (Mo) while the second conductive layer includes at least one selected from the group consisting of aluminum (Al), copper (Cu), chromium (Cr), tantalum (Ta), titanium (Ti), tungsten (W), molybdenum (Mo), an n-type silicon containing phosphorus, and silicide.

55. (Currently Amended) A semiconductor device having an active matrix display device, said display device comprising:

~~at least one~~ a first thin film transistor formed over ~~a substrate~~ an insulating surface;

a pixel electrode electrically connected to said first thin film transistor;

a driver circuit including ~~at least one~~ a second thin film transistor formed over said ~~substrate~~ insulating surface for driving said ~~at least one~~ first thin film transistor,

wherein said first thin film transistor ~~comprising~~ comprises:

a semiconductor island ~~[[on an]]~~ over the insulating surface;

source and drain regions formed in the semiconductor island;

a channel forming region in the semiconductor island between the source and drain regions;

a pair of lightly doped regions formed between the channel forming region and the source and drain regions wherein an impurity concentration in the lightly doped regions is smaller than that in the source and drain regions;

a gate electrode formed over the semiconductor island with a gate insulating film interposed ~~therebetween~~ therebetween.

wherein said gate electrode comprises at least a first conductive layer and a second conductive layer formed on the first conductive layer, said first conductive layer having a pair of tapered ~~portions~~, portions which extend beyond side edges of the second conductive layer,

wherein the pair of lightly doped regions has a pair of first portions which are overlapped by the pair of tapered portions of the first conductive layer, and a pair of second portions which extend beyond side edges of the first conductive layer, and ~~[[the]] an impurity concentration of said impurity~~ an impurity concentration in the pair of first portions monotonically increases in a direction from said channel forming region toward the source and drain regions.

56. (Currently Amended) A device according to claim 55, wherein an angle between top surfaces of the tapered portions of the first conductive layer and a top surface of the gate insulating film is in a range of 3 to 60 degrees.

57. (Currently Amended) A device according to claim 55, wherein the semiconductor island ~~[[is a]]~~ comprises crystalline silicon island.

58. (Previously Presented) A device according to claim 55, wherein the first conductive layer includes at least one selected from the group consisting of chromium (Cr), tantalum (Ta), an n-type silicon containing phosphorus, titanium (Ti), tungsten (W), and molybdenum (Mo) while the second conductive layer includes at least one selected from the group consisting of aluminum (Al), copper (Cu), chromium (Cr), tantalum (Ta), titanium (Ti), tungsten (W), molybdenum (Mo), an n-type silicon containing phosphorus, and silicide.

59. (Previously Presented) A device according to claim 55, wherein the semiconductor device is one selected from the group consisting of a video camera, a digital camera, a rear-type projector, a front-type projector, a head mount display, a goggle-type display, a navigation system for vehicles, a personal computer, a mobile computer, a cellular phone, and an electronic book.

60. (Currently Amended) A semiconductor device having an active matrix display device, said display device comprising:

~~at least one~~ a first thin film transistor formed over ~~a substrate~~ an insulating surface;

a pixel electrode electrically connected to said first thin film transistor;

a driver circuit including ~~at least one~~ a second thin film transistor formed over said ~~substrate~~ insulating surface for driving said ~~at least one~~ first thin film transistor,

wherein said second thin film transistor ~~comprising~~ comprises:

a semiconductor island over ~~[[an]]~~ the insulating surface;

source and drain regions formed in the semiconductor island;

a channel forming region in the semiconductor island between the source and drain regions;

a pair of lightly doped regions formed between the channel forming region and the source and drain regions wherein an impurity concentration in the lightly doped regions is smaller than that in the source and drain regions;

a gate electrode formed over the semiconductor island with a gate insulating film interposed ~~therebetween~~ therebetween,

wherein said gate electrode comprises at least a first conductive layer and a second conductive layer formed on the first conductive layer, said first conductive layer having a pair of tapered ~~portions~~, portions which extend beyond side edges of the second conductive layer,

wherein the pair of lightly doped regions has a pair of first portions which are overlapped by the pair of tapered portions of the first conductive layer, and a pair of second portions which extend beyond side edges of the first conductive layer, and ~~[[the]] an impurity concentration of said impurity~~ in the pair of first portions monotonically increases in a direction from said channel forming region toward the source and drain regions.

61. (Currently Amended) A device according to claim 60, wherein an angle between top surfaces of the tapered portions of the first conductive layer and a top surface of the gate insulating film is in a range of 3 to 60 degrees.

62. (Previously Presented) A device according to claim 60, wherein the first conductive layer includes at least one selected from the group consisting of chromium (Cr), tantalum (Ta), an n-type silicon containing phosphorus, titanium (Ti), tungsten (W), and molybdenum (Mo) while the second conductive layer includes at least one selected from the group consisting of aluminum (Al), copper (Cu), chromium (Cr), tantalum (Ta), titanium (Ti), tungsten (W), molybdenum (Mo), an n-type silicon containing phosphorus, and silicide.

63. (Previously Presented) A device according to claim 60, wherein the semiconductor device is one selected from the group consisting of a video camera, a digital camera, a rear-type projector, a front-type projector, a head mount display, a goggle-type display, a navigation system for vehicles, a personal computer, a mobile computer, a cellular phone, and an electronic book.

64. (Currently Amended) A semiconductor device having an active matrix display device, said display device comprising:

~~at least one~~ a first thin film transistor formed over ~~a substrate~~ an insulating surface;

a pixel electrode electrically connected to said first thin film transistor;

a driver circuit including ~~at least one~~ a second thin film transistor formed over said ~~substrate~~ insulating surface for driving said ~~at least one~~ first thin film transistor, each of the first and second thin film transistors comprising:

a semiconductor island over ~~[[an]]~~ the insulating surface;

source and drain regions formed in the semiconductor island;

a channel forming region in the semiconductor island between the source and drain regions;

a pair of lightly doped regions formed between the channel forming region and the source and drain ~~regions~~ regions, wherein an impurity concentration in the lightly doped regions is smaller than that in the source and drain regions;

a gate electrode formed over the semiconductor island with a gate insulating film interposed ~~therebetween~~ therebetween,

wherein said gate electrode comprises at least a first conductive layer and a second conductive layer formed on the first conductive layer, said first conductive layer having a pair of tapered ~~portions~~, portions which extend beyond side edges of the second conductive layer,

wherein the pair of lightly doped regions has a pair of first portions which are overlapped by the pair of tapered portions of the first conductive layer, and a pair of second portions which extend beyond side edges of the first conductive layer, and ~~[[the]]~~ an impurity concentration ~~of said impurity~~ in the pair of first portions monotonically increases in a direction from said channel forming region toward the source and drain regions.

65. (Currently Amended) A device according to claim 64, wherein an angle between top surfaces of the tapered portions of the first conductive layer and a top surface of the gate insulating film is in a range of 3 to 60 degrees.

66. (Currently Amended) A device according to claim 64, wherein the semiconductor island ~~[[is a]]~~ comprises crystalline silicon ~~island~~.

67. (Previously Presented) A device according to claim 64, wherein the first conductive layer includes at least one selected from the group consisting of chromium (Cr), tantalum (Ta), an n-type silicon containing phosphorus, titanium (Ti), tungsten (W), and molybdenum (Mo) while the second conductive layer includes at least one selected from the group consisting of aluminum (Al), copper (Cu), chromium (Cr), tantalum (Ta), titanium (Ti), tungsten (W), molybdenum (Mo), an n-type silicon containing phosphorus, and silicide.

68. (Previously Presented) A device according to claim 64, wherein the semiconductor device is one selected from the group consisting of a video camera, a digital camera, a rear-type projector, a front-type projector, a head mount display, a goggle-type display, a navigation system for vehicles, a personal computer, a mobile computer, a cellular phone, and an electronic book.

69. (Previously Presented) A device according to claim 13 wherein said active matrix display device is a liquid crystal device.

70. (Previously Presented) A device according to claim 46 wherein said active matrix display device is a liquid crystal device.

71. (Previously Presented) A device according to claim 51 wherein said active matrix display device is a liquid crystal device.

72. (Previously Presented) A device according to claim 55 wherein said active matrix display device is a liquid crystal device.

73. (Previously Presented) A device according to claim 60 wherein said active matrix display device is a liquid crystal device.

74. (Previously Presented) A device according to claim 64 wherein said active matrix display device is a liquid crystal device.

75. (Previously Presented) A device according to claim 13 wherein said active matrix display device is an electroluminescent display device.

76. (Previously Presented) A device according to claim 46 wherein said active matrix display device is an electroluminescent display device.

77. (Previously Presented) A device according to claim 51 wherein said active matrix display device is an electroluminescent display device.

78. (Previously Presented) A device according to claim 55, wherein said active matrix display device is an electroluminescent display device.

79. (Previously Presented) A device according to claim 60 wherein said active matrix display device is an electroluminescent display device.

80. (Previously Presented) A device according to claim 64 wherein said active matrix display device is an electroluminescent display device.

81. (Previously Presented) A device according to claim 60, wherein the semiconductor island comprises crystalline silicon island.

82. (Previously Presented) A device according to claim 51, wherein the semiconductor device is one selected from the group consisting of a video camera, a digital camera, a rear-type projector, a front-type projector, a head mount display, a goggle-type display, a navigation system for vehicles, a personal computer, a mobile computer, a cellular phone, and an electronic book.